

Fig. 1

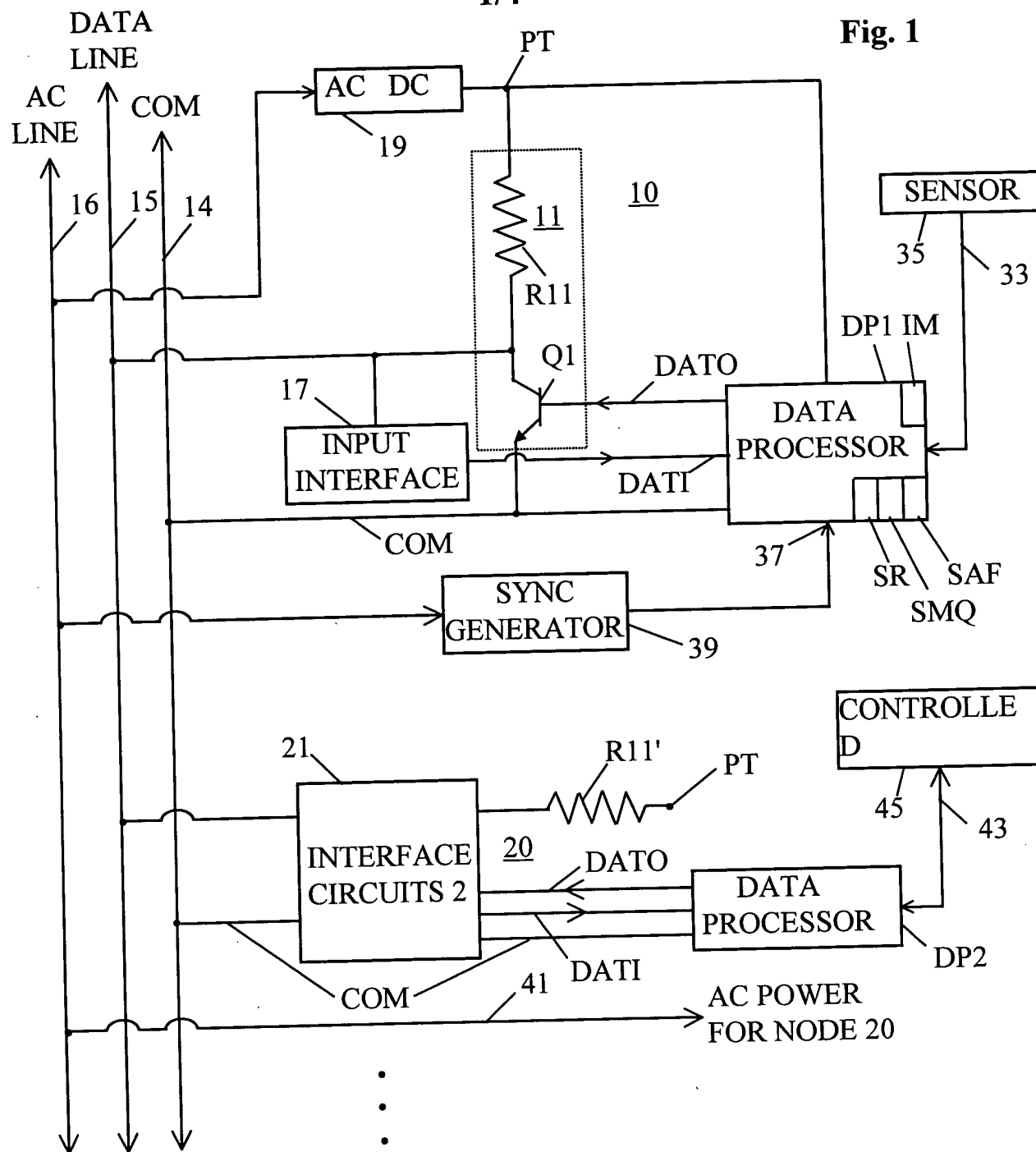
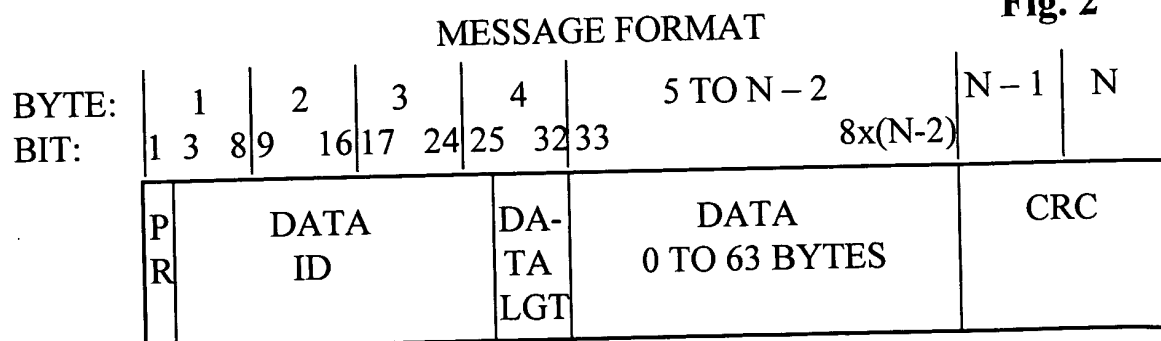


Fig. 2



FROM POWER-ON  
INTERRRUPT

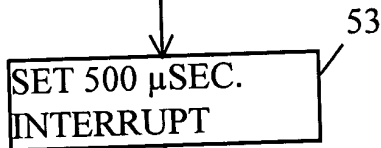
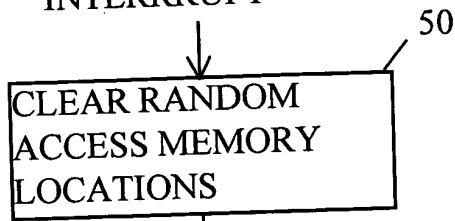


Fig. 4a

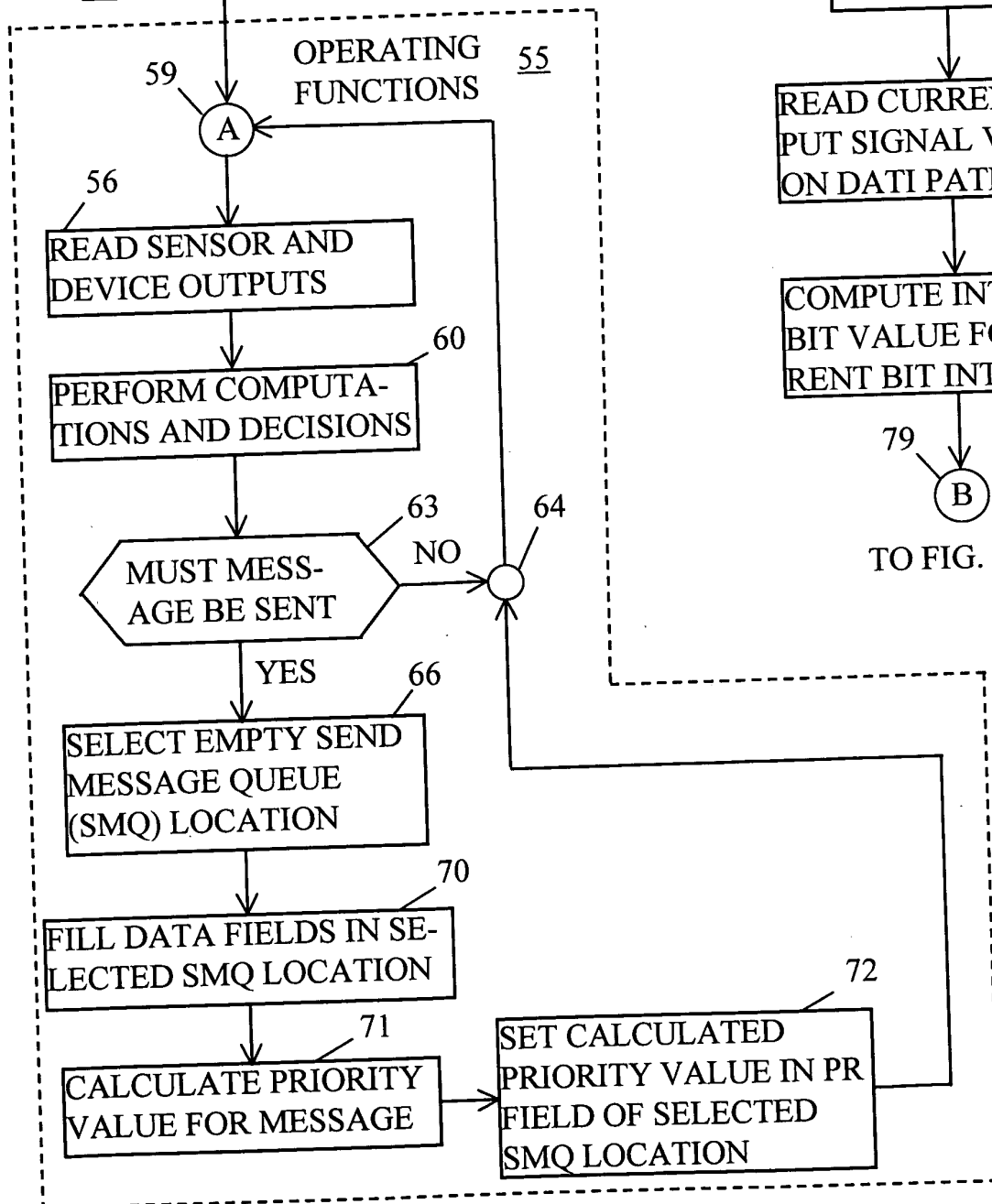
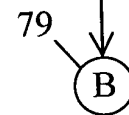
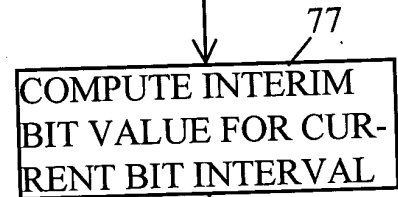
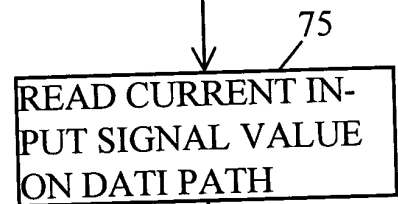
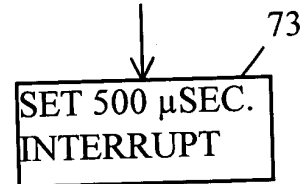


Fig. 4b

FROM 500 μSEC.  
INTERRUPT



TO FIG. 4c

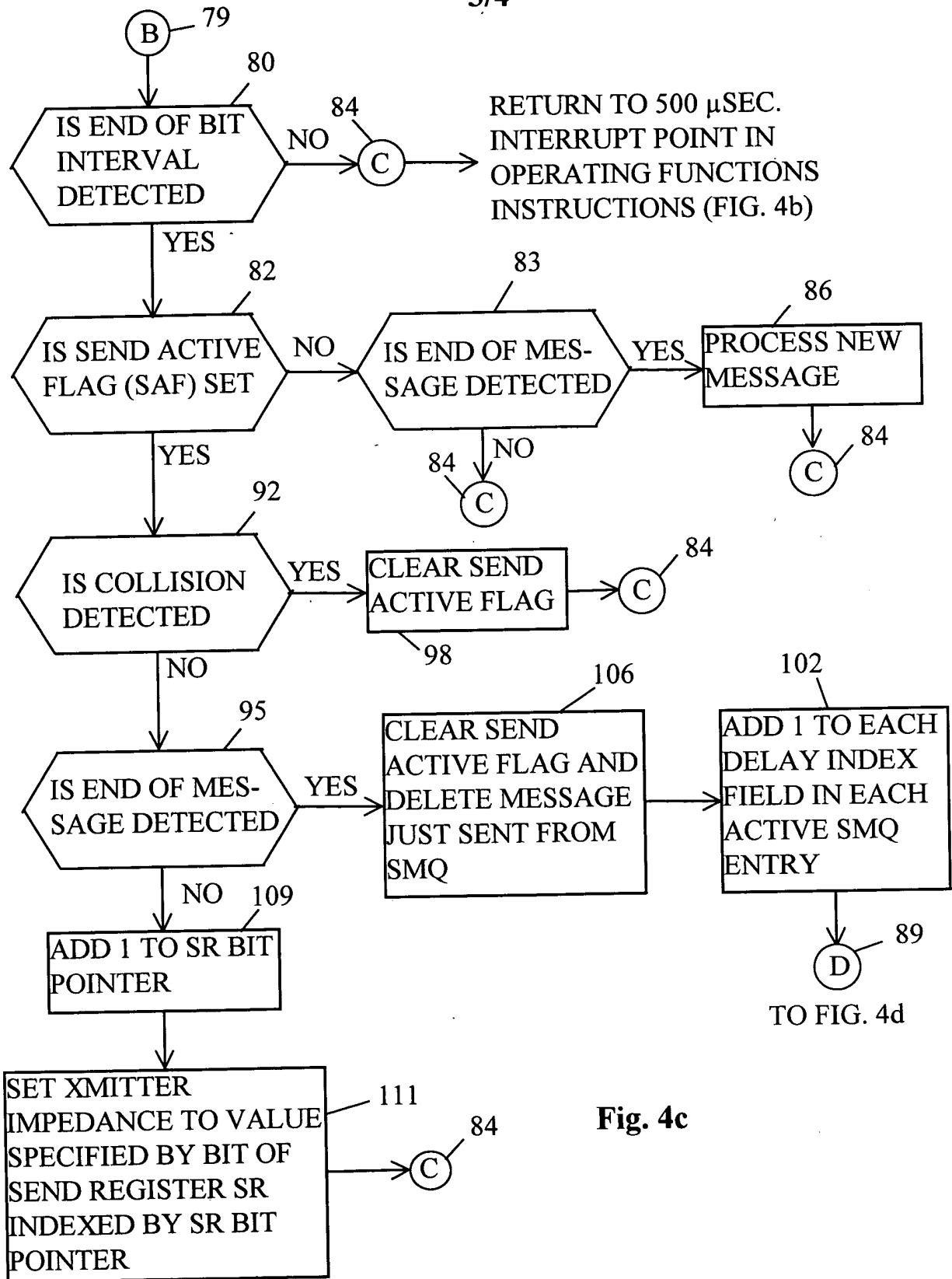
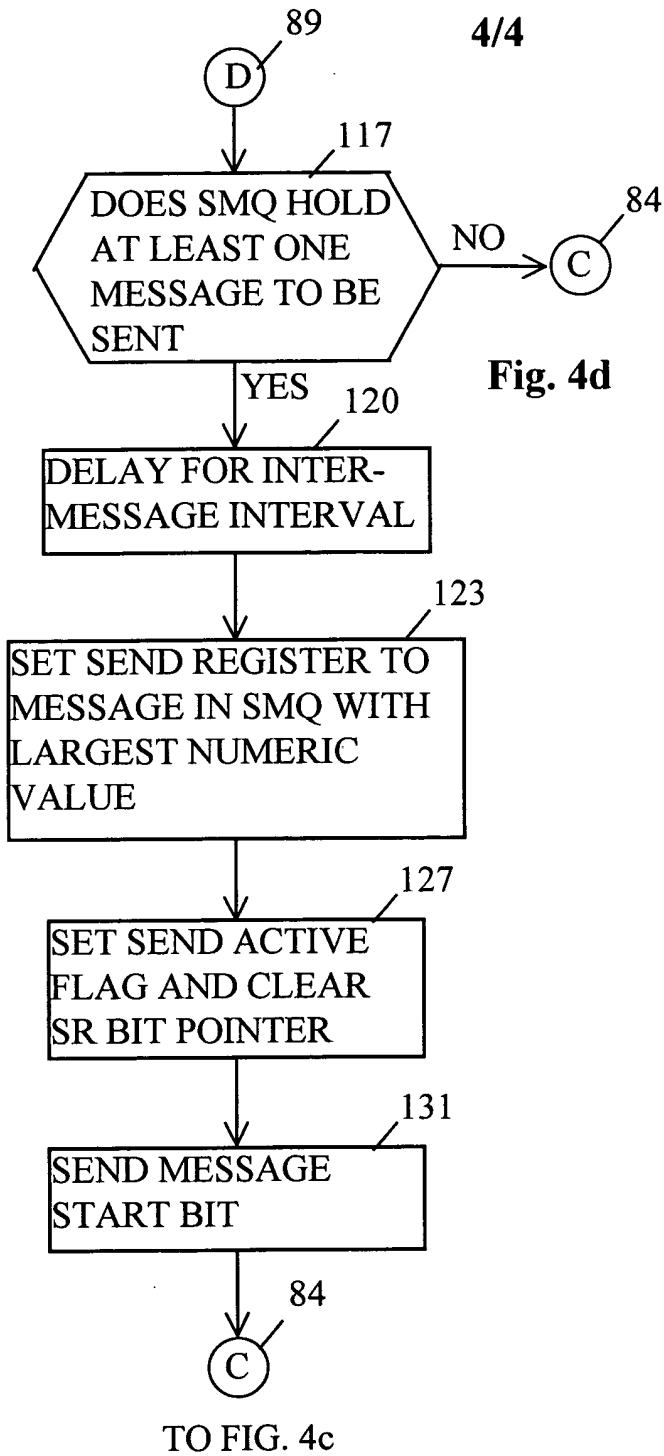


Fig. 4c



SEND MESSAGE  
QUEUE FORMAT

1	PR	DI	DATA ID - CRC
2			
3			
.			
.			
.			
8			

Fig. 3